



Finanziato
dall'Unione europea
NextGenerationEU



Ministero
dell'Università
e della Ricerca



Italiadomani
PIANO NAZIONALE
DI RIPRESA E RESILIENZA



SERICS
SECURITY AND RIGHTS IN THE CYBERSPACE

SERICS PhD School on Advanced Hardware Security

From Sunday, October 26th to Friday, October 31st, 2025
Campus of Turin of the International Training Centre of the ILO
Viale Maestri del Lavoro, 10, 10127 Torino TO, Italy

	27-ott	28-ott	29-ott	30-ott	31-ott
9:00-10:45	<i>Emanuele Valea, CEA-List</i> Cryptography: Classical and Post-Quantum	<i>Nicolas Belleville, CEA-List</i> Side-Channel Attacks	<i>Ioana Vatajelu, CNRS</i> Physical Unclonable Functions and True Random Number Generators	<i>Brice Colombier, Université Jean Monnet</i> Fault Attacks	<i>Maria Mendez Real, Université Bretagne Sud</i> Micro-Architectural Attacks
10:45-11:15	COFFEE BREAK				
11:15-13:00	<i>Emanuele Valea, CEA-List</i> Cryptography: Classical and Post-Quantum	<i>Nicolas Belleville, CEA-List</i> Side-Channel Attacks	<i>Ioana Vatajelu, CNRS</i> Physical Unclonable Functions and True Random Number Generators	<i>Brice Colombier, Université Jean Monnet</i> Fault Attacks	<i>Roberto Baldoni, Agenzia per la Cybersicurezza Nazionale – ACN, Washington DC</i> The Geopolitical Role of Hardware in Security
13:00-14:00	LUNCH				
14:00-15:45	<i>Giorgio Di Natale CNRS</i> Trust Issues in Electronics	<i>Samuele Yves CERINI, CINI Cybersecurity National Lab</i> Hands-on on Side-Channel Attacks	<i>Sergio Vinagrero, CNRS</i> Hand-on on PUFs and TRNGs	Hand-on on Fault Attacks	Group Project Presentations
15:45-16:15	COFFEE BREAK				
16:15-18:00	<i>Nicolò Maunero, Paolo Prinetto CINI cybersecurity National Lab</i> Group Project Launch	<i>Samuele Yves CERINI, CINI Cybersecurity National Lab</i> Hands-on on Side-Channel Attacks	<i>Sergio Vinagrero, CNRS</i> Hand-on on PUFs and TRNGs	Hand-on on Fault Attacks	Group Project Presentations
19:30-21:00	DINNER				